

## CURRICULUM VITAE

### Personal information

Name

**Filippo La Vecchia**

Photo



Permanent Address

Phone

E-mail

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Nationality

Italian

Date of birth

Oct 2016 - today

Employee for Italian government as “IT public official”. I’m currently in charge for the “Agency of Territorial Cohesion”, a new agency of the Italian government whose activity is oriented to the implementation and actuation of UE programs. So my duty and responsibility is to represent the Italian state as national authority and check and verify that the resources coming from the UE are well allocated in all the projects related to the European International Cooperation. I’m involved in the following CTE Programs inside the Monitoring Committees and the National Committees: Interreg Central Europe, Interreg Alpine Space, Interreg Italy-Slovenija, Espon

Employee at ST Microelectronics Catania as Product Engineer.

Jul 2000 – Sep 2016

- In charge as Product Engineer in MCD, Microcontroller division. I’m following STM32 devices already in production to improve their yield and also I’m trying to bring to mass production new products. I’m in charge for test program/flow development and optimization, test simulation (simvision), hw testing on Verigy HP93k–SOC and Advantest T2000 platforms. I work, using UNIX, on svn database to manage testing programs that will be used in production.
- My previous experience in ST I was in charge as Product Engineer for IGBT power modules. I qualified a new family of devices, the IPM (intelligent power modules) products. They are used to drive the three-phases motors for the following applications: air conditioning, washing machines and dishing machines. My main activities were oriented to provide the design of the testing boards, write the testing program on all the available platforms (testers), choose the material (resin, glue, leadframe, wires) to reach the maturity of the package, prepare the documentation (MBD,TFI,TPD,BSA) for the maturity of the device, follow the ramp-up at the subcontractors or internal plants. For this last reason I was many times in mission abroad.I did two mission in Hsinchu (Taiwaan) on Feb 2008 and May 2008 to release in production the IPM in our subcontractor.I also was in charge for the qualification of new production line in.ST Shenzhen. I was there 5 times in the years 2010,2011,2012 and.I qualified 6 new products..I went also in Nantong (close Shangai) Fujitsu 2 times where I prepared.the testing for new IPMs.

- In my first experience , I was in charge as Process Engineer for the EWS department at ST, Catania. I was in charge to create procedures and to design systems in order to optimize the process involved into the Testing of Electronic Devices at
- In my first experience , I was in charge as Process Engineer for the EWS department at ST, Catania. I was in charge to create procedures and to design systems in order to optimize the process involved into the Testing of Electronic Devices at

Wafer Level (EWS). I was also in charge to write and debug testing programs of Power Mos devices.

### MAIN SKILLS:

#### Knowledge “as product engineer”:

Yield Explorer Synopsis tool for yield and statistical analysis.

Galaxy-examinator tool for yield and statistical analysis.

Yield improvement methodology and experience.

DOE methodology for new product qualification.

Quality School for ICs failure analysis.

Handling of customer returns (8D methodology).

Reliability trials and plans knowledge and support.

Transfer role of new products to the plants for their qualification and ramp-up and mass production.

PCB and loadboard/jig design using ORCAD tool.

#### Knowledge “as testing engineer” of the following testers:

Advantest T2000, Verigy HP V93000-SOC , Microflex/UltraFlex Teradyne, J750 Teradyne, Eagle Teradyne ETS-346, Eagle Teradyne 600, SPEA C430MX, Accotest, Credence-LTX ASL1k/3k, Tesec, SZ, QT200-ST.

I have a good knowledge on several kind of products like Smart Power, Vpower, Power Devices (PowerMOS, IGBT, IPM that is like a system on package with IGBTs, drivers, diodes and passive components inside) and Voltage Regulator. I also used Teradyne A585 and Catalyst as EWS testing engineer.

#### Knowledge of simulation tool:

Simvision : used for test simulation of STM32 Microcontroller.

#### Seminars and special courses as testing engineer:

“ Verigy V93000 SOC Basic User training”

“Microflex/UltraFlex programming fundamentals”

“Eagle Teradyne ETS-346 Basic programming”

“Eagle Teradyne ETS-346 Advanced programming”

“Teradyne J750 Basic Programming”

“QT 200 Smart Power, Voltage Regulator”, carried out at STMicroelectronics, Catania, from STM Automation Group of Agrate;

“Testing of Radiofrequency devices, structure and programming of tester Teradyne A585 and Catalyst”, carried out at STMicroelectronics, Catania, from TPA-RF Product and Testing Group.

Training on SPEA C430MX in week 7 , year 2010

Training for Eagle ETS88/200 in week 7 , year 2010

“QT 200 Smart Power, Voltage Regulator”, at STMicroelectronics Catania, by STM Automation Group - Agrate.

“Testing di dispositivi Radiofrequenza, structure and programming of tester Teradyne A585 e Catalyst”, at STMicroelectronics Catania, by TPA-RF Product and Testing Group.

Other attended courses:

“Basic Java” from 02/26/2007 to 03/02/2007 (24h) - Catania.

“Metrologia” from 02/19/2007 to 03/12/2007 (32h) - Catania. “Product Development Process” 12/13/2006 (8h) - STU University “Corp Resp Aw\_italian” 11/15/2006 at STU University “Genealogia, Codifica e Document” 06/12/2006 (4h) - Catania “Traceability In St” 12/19/2005 (3h) Catania

“Emas” 02/18/2005 (2h ) - Catania

“Ohsas” 12/14/2004 (2h) - Catania

“Business Excellence @ St” 04/20/2004 (6h) at STU University

“Fmea” 02/28/2001 (3h) -Catania

## Previous experiences

### **February 2000 – June 2000**

Teacher of Phisic and Teacher of Phisic and Math at the Liceo Classico Statale “Umberto I” in Palermo.

### **Sep 99 – Dec 99**

Attended a course of programming in Cobol for AS400 at the Center IBM of Novedrate (CO).

### **September 1997 – December 1998”:**

Officer of the "Italian Navy", with the duty of “*Route Officer*” , on the military ship Brenta. Between April 1998 and September 1998 i took part of the mission "Alba II" in Albania for the NATO.

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Officer of the "Italian Navy", with the duty of “*Route Officer*” , on the military ship Brenta. Between April 1998 and September 1998 i took part of the mission "Alba II" in Albania for the NATO.

## **Education**

1991 – 1997  
07.10.1997

Student at the University of Palermo, Faculty of Electronic Engineering  
**Degree in Electronic Engineering**, specialization Automatic Control  
Final Degree Result: 107/110

1986 – 1990  
07.17.1990

"Liceo Classico Statale 'U. Foscolo' ”, Canicattì (AG)  
Diploma result: 60/60

### **Languages:**

#### Mother Tongue

Italian

#### Other languages

English : good level written and spoken  
French: scholastic

### **IT Skills:**

Programming Languages  
OperativeSystems  
Tools

C, C++, Java, Cobol, Visual Basic, VHDL  
Windows 7/NT/2000/XP, UNIX  
Matlab, Simulink, Orcad, Office

### **Full Driving Licence**

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### **Personal Interests:**

Music

The treatment of personal information is authorized according to Law 675/96 on Privacy.

Rome, 2017– 02– 24

Signature  
Filippo La Vecchia